

In the Specification

Page 1, line 12, ~~replace~~ "frame" with --frames--.

Page 2, line 3, ~~replace~~ "frame" with --frames--.

Page 4, line 1, ~~after~~ "and", insert --a--.

Page 5, line 15, ~~replace~~ "MB" with --macroblock (MB)--.

Page 6, line 8, after "memory", insert --(RAM1)--;

line 9, after "memory", insert --(RAM2)--;

line 12, after "first", insert --memory--;

line 13, after "or", insert --the--;

line 16, after "memory", insert --(RAM3)--;

line 17, after "block 20,", insert --as shown in FIG.1,--; and

Page 7, line 4, after "output data", insert --(26, 28, 30)--;

line 8, replace "16 and second 18 memories" with --memory 16 and the second

A1 memory 18-- and

line 15, after "block", insert --10--.

Page 9, line 4, after "DYD", insert --,--, replace "=" with --is--;

line 11, replace "16 and second 18 memories" with --memory 16 and the second

A2 memory 18--, after "RAM", insert --, respectively--;

line 15, after "first", insert --memory--; and

line 16, replace "18 memory" with --memory 18--.

Page 10, line 4, replace "DQ and IDCT blocks" with --dequantization block 10 and the

A3 IDCT block 12--;

line 11, after "coefficient" insert --26--, after "index", insert --28--;

line 12, after "scale", insert --30--, after "matrix", insert --32--;

line 15, after "block", insert --14--; and

line 17, replace "mblock" with --macroblock--.

Page 11, line 2, after "multiplexer", insert --34--;

line 5, replace "flip flop" with --flip-flop 52--;

line 6, after "matrix", insert --32--, after "index", insert --28--;

line 7, after "standard matrix", insert --32--, after "dequantization matrix", insert --
T--;

line 12, after "multiplier", insert --54--;

line 13, after "flip-flop", insert --52--; and

line 14, after "line", insert --68--, replace "multiplexer 68" with --multiplexer 62--.

Page 12, line 1, after "flip-flop", insert --72--, after "scale", insert --30--;

line 2, before "multiplier", insert --second--;

line 6, after "multiplier", insert --74--;

line 7, replace "DC and intra" with --a DC_AND_INTRA indicator--;

line 8, after "line", insert --90--;

line 9, replace "90" with --84--; and

line 15, after "multiplexer", insert --84--.

Page 13, line 8, after "first", insert --memory (RAM1)--;

line 6, before "second", insert --the--, after "memory" insert --(RAM2), referred to
as a memory--, after "118", insert --as shown in FIG.2--; and

line 12, after "first", insert --memory (RAM1)--, replace "second memory" with --
the second memory (RAM2)--.

Page 15, line 20, after "cosine matrix", insert --DD^t--;

line 21, before "have", insert --T--, after "cosine matrix", insert --DD^t--;

line 23, after "Since", insert --matrix--.

Page 16, line 8, after "first", insert --memory (RAM1)--, after "or", insert --the--, after

"second memory", insert --(RAM2), referred to as a memory 126 in FIG. 3--;

line 11, after "first memory" insert --(RAM1)--, after "second memory", insert --
(RAM2) 126--;

line 12, after "memory", insert --22--, after "block", insert --20, as explained

referring to FIG.1--;

line 13, after " memory", insert --126--; and

line 21, after "memory", insert --respectively--.

Page 19, line 5, after "first", "second", and "third", insert --multiplexer--, respectively,

before "second", "third", and "fourth", insert --a--, respectively, replace "158
multiplexer" with --multiplexer 158--;

line 13, after "multiplexer", insert --152--;

line 14, after the both "multiplexer"s, insert --152--, respectively;

line 15, after "multiplexer", insert --152--;

line 16, after "multiplexer", insert --154--;

line 17, after the both "multiplexer"s, insert --154--, respectively;

line 18, after "multiplexer", insert --154--;

line 19, after "multiplexer", insert --156--;

line 20, after "multiplexer", insert --156--; and

line 21, after the both "multiplexer"s, insert --156--, respectively.

Page 20, line 1, after "multiplexer", insert --158--;

line 2, after "multiplexer", insert --158--;

line 3, after the both "multiplexer"s, insert --158--, respectively;

line 4, delete "178 and ", before "186", insert --d_a 178, and d_b";

line 5, after "multiplexers", insert --, respectively--, after "memory", insert --126--;

line 10, after "to", insert --the output of--, replace "multiplexer output" with

--multiplexer 156--;

line 11, after "to", insert --the output of--, replace "multiplexer output" with

--multiplexer 158--, after "The output", insert --of the first subtractor

194--;

line 13, replace "inputs" with --data inputs 180 and 188--, after 158, insert --,

respectively--, after "and the" insert --resultant--;

line 15, after "to" insert --the output of--;

line 16, replace the first "output" with --152--, after "to", insert --the output of--,

replace "multiplexer output" with --multiplexer 154--; and

line 17, replace "adder" with --first adder 202--.

Page 21, line 1, after "inputs", insert -- 164, 172--, after "154", insert --, respectively--,

line 5, replace "first" with --a first input--, replace "second inputs" with --a second

input--;

line 11, after "to", insert --the addresses--;

line 12, after "to", insert --the addresses--;

line 13, after "block", insert --20 of FIG.1--;

line 15, after "through", insert --the outputs--;

line 16, after "by", insert --the outputs--;

line 17, after "by", insert --the outputs--;

line 18, after "memory", insert --126 of FIG.3--; and

line 21, after the first "block", insert --20--.

Page 22, line 1, after "fifth", "sixth", and "seventh", insert --multiplexer--, respectively,

before "sixth" and "seventh", insert --a--, respectively;

line 2, replace "eighth 236 multiplexer" with --an eighth multiplexer 236--;

line 4, after "multiplexers", insert --230, 232, 234, and 236--;

line 6, replace "208" with --202--;

line 8, after "multiplexers", insert --230, 232, 234, and 236--;

line 12, after "multiplexer", insert --230--;

line 13, after the both "multiplexer"s, insert --230--, respectively;

line 14, after "multiplexer", insert --230--;

line 15, before "is x5", insert --230--, before "is s3", insert --232--;

line 16, after "multiplexer", insert --232--;

line 17, after the both "multiplexer"s, insert --232--, respectively;

line 18, after "multiplexer", insert --232--;

line 19, after the both "multiplexer"s, insert --234--, respectively;

line 20, after "multiplexer", insert --234--; and

line 21, after the both "multiplexer"s, insert --234--, respectively.

Page 23, line 1, after "multiplexer", insert --236--;

line 2, after the both "multiplexer"s, insert --236--, respectively;

line 3, after "multiplexer", insert --236--;

line 4, after "multiplexer", insert --236--;

line 5, after "to", insert --the output of--;

line 6, replace "multiplexer output" with --multiplexer 234--, after "to", insert --the output of--;

line 7, replace "output, and" with --236, and--, after "The output", insert --of the second subtractor 280--;

line 9, after "Therefore", insert --, for example,--;

line 10, after "to", insert --the output of--;

line 11, replace "output" with --230--, after "to", insert --the output of--;

line 12, replace "output, and" with --232, and--, replace "adder" with --second adder 290--; and

line 15, after "output", insert --of the seventh flip-flop 296--.

Page 24, line 1, after "subtractor" insert --194--, after "or", insert --the--, after "flip-flop", insert --200--;

line 2, after "subtractor", insert --280--, after "or", insert --the--, after "flip-flop", insert --286--, after "306", insert --of the MAC 302--;

line 11, replace "," with --and--, after "multiplexers" insert --, respectively,--;

line 20, after "memory" insert --126 of FIG.3--;

line 21, after "block", insert --218--; and

line 22, after "block", insert --20 of FIG.1--.

Page 25, line 1, after "block" insert --218--;

line 2, after "block", insert --12--;